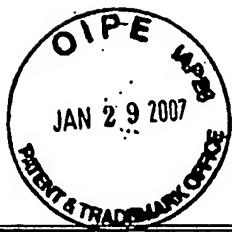




SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION PTO-1449			ATTY. DOCKET NO. SUN-P9325-MEG	APPLICATION NO. 10/637,169		
			APPLICANT Marc Tremblay, et al.			
			FILING DATE 8 August 2003		GROUP ART UNIT 2183	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
RF	2004/0162967 A1	19 Aug 2004	Tremblay et al.	712	228	8 Aug 03
RF	2004/0187123 A1	23 Sept 2004	Tremblay et al.	718	100	23 Jan 04
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						Yes
						No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
RF	"Hybrid Hardware/Software Transactional Memory", by Mark Moir et al., XP-002407376.					
RF	"Hybrid Transactional Memory", by Mark Moir, Sun Microsystems, Inc. 2005, XP-002407375.					
EXAMINER	/Robert Fennema/		DATE CONSIDERED 03/01/2007			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



SHEET 1 OF 1

INFORMATION DISCLOSURE
CITATION

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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

RF	"Lock-Based Programs and Transactional Lock-Free Execution", by Ravi Rajwar et al., University of Wisconsin-Madison Technical Report #1440, April 2002.
RF	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F Martinez et al., Workshop on Memory Performance Issues, International Symposium on Computer Architecture, June 2001.
RF	"Transactional Memory: Architectural Support for Lock-Free Data Structures", by Maurice Herlihy.
RF	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F. Martinez et al., Technical Report UIUCDCS-R-2001-2202, February 2001.
RF	"Transactional Lock-Free Execution of Lock-Based Programs", by Ravi Rajwar, Proceedings of the Tenth International Conference on Architectural Support for Programming Languages and Operating Systems, Oct. 6-Oct. 9, 2002, San Jose, CA.

EXAMINER	/Robert Fennema/	DATE CONSIDERED
		03/01/2007

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